



Vivekanand Education Society's
INSTITUTE OF TECHNOLOGY

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[Affiliated to University Of Mumbai , Approved by AICTE and Recognised By Govt. Of Maharashtra]

ADMISSION TO Ph.D. PROGRAMME
2015-16

Applications are invited from eligible candidates for **admission to Ph.D. Programme** of the faculty of Technology for the year 2015-16 in **Electronics Engineering** in V. E. S. Institute of Technology, recognized Ph.D. CENTRE of University of Mumbai.

Eligibility : As per the Ordinances O.5473, O 5474, O 5476 of the University of Mumbai as mentioned in VCD No.Exam./Thesis/ Univ./VCD/2414 of 2010 and Circular No.UG/441 of 2005. (Copies can be downloaded from www.vesit.edu).

Application form can be obtained and submitted in person from **15.6.2015 to 29.6.2015** from Admin. Office of VESIT by payment of Rs.100/- in cash or DD drawn in favour of VESIT payable at Mumbai between 11.00 a.m. and 4.00 p.m. (Except Sunday & Bank Holiday) .

Admissions will be based on the rules and Regulation of University of Mumbai for Admission in Ph.D. Programme in faculty of Technology.

s/d
Principal