

VIVEKANAND EDUCATION SOCIETY INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING
Time Table for Internal Assessment KT Test August 2019

SEMESTER /DATE	11/09/2019	13/09/2019	14/09/2019	16/09/2019	
	9:30am-10:30am	9:30am-10:30am	9:30am-10:30am	9:30am-10:30am	1:30pm-2:30pm
SEM III(Choice Based)	AM-III	EIC	----	DSD,CTN	EDC-1
SEM IV(Choice Based)	----	AM-IV	LIC	SS	PCE
SEM V (Choice Based)	MICROELECTRONICS	----	----	----	----
SEM VI(CBSGS)	DTSP,VLSI DESIGN	----	----	----	----
SEM VI (Choice Based)	ARWP	IPMV, RADAR ENGG	DVLSI	----	----
SEM VII(CBSGS)	IVP	MRE	NNFL	----	----
SEM VIII(CBSGS)	----	----	----	SCN	IVC

Dr. Rawat C. D.
(Exam in-charge EXTC Dept)

Mrs. Monali Chaudhari
(Exam in-charge EXTC Dept)

Seating Arrangement : Room No. CA4

